

*“Perspective of tunnel-FET for future low-power technology nodes”*. Verhulst AS, Verreck D, Smets Q, Kao K-H, Van de Put M, Rooyackers R, Sorée B, Vandooren A, De Meyer K, Groeseneken G, Heyns MM, Mocuta A, Collaert N, Thean AV-Y, 2014 Ieee International Electron Devices Meeting (iedm) (2014)