

*“Optimization of gate-on-source-only tunnel FETs with counter-doped pockets”*. Kao K-H, Verhulst AS, Vandenberghé WG, Sorée B, Magnus W, Leonelli D, Groeseneken G, De Meyer K, IEEE transactions on electron devices **59**, 2070 (2012).  
<http://doi.org/10.1109/TED.2012.2200489>