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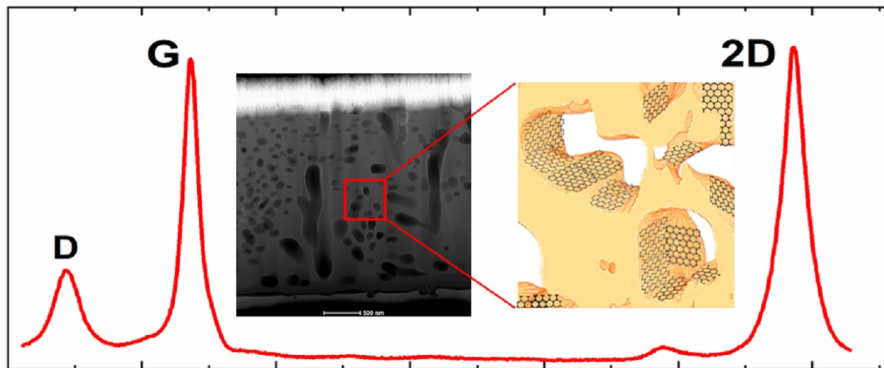
Reference:

Veronesi Stefano, Pfusterschmied Georg, Fabbri Filippo, Leitgeb Markus, Arif Omer, Arenas Esteban Daniel, Bals Sara, Schmid Ulrich, Heun Stefan.- 3D arrangement of epitaxial graphene conformally grown on porousified crystalline SiC
Carbon - ISSN 1873-3891 - 189(2022), p. 210-218
Full text (Publisher's DOI): <https://doi.org/10.1016/J.CARBON.2021.12.042>
To cite this reference: <https://hdl.handle.net/10067/1865830151162165141>

Graphical Abstract

3D arrangement of epitaxial graphene conformally grown on porousified crystalline SiC

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Highlights

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- Native 3D epitaxial graphene with high quality
- Realized in nano-porous crystalline 4H-SiC(0001) backbone
- Tunable pores dimension via photo(electro)chemical etching
- (HAADF-STEM) 3-D characterization of graphene in the porous structure
- Raman analysis of graphene

3D arrangement of epitaxial graphene conformally grown on porousified crystalline SiC

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Abstract

Nanoporous materials represent a versatile solution for a number of applications ranging from sensing, energy applications, catalysis, drug delivery, and many others. The synergy between the outstanding properties of graphene with a three-dimensional porous structure, circumventing the limits of its 2D nature, constitutes therefore a breakthrough for many fields. We report the first three-dimensional growth of epitaxial graphene on a porousified crystalline 4H-SiC(0001). The wafer porosification is performed via a sequence of metal-assisted photochemical and photoelectrochemical etching in hydrofluoric acid based electrolytes. Pore dimensions of the matrix have been evaluated by electron tomography resulting in an average diameter of 180 nm. Graphene growth is performed in an ultra high vacuum environment at a base pressure of 10^{-11} mbar. The graphene growth inside the pores is uniform as confirmed by Transmission Electron Microscopy (TEM) analysis. Raman spectroscopy confirms the high quality of the graphene with a 2D/G ratio > 1 and an average graphene crystal size of ≈ 100 nm. Furthermore, it demonstrates a uniform coverage of graphene across the whole sample area **accessible to the Raman probe**. The surface-to-volume ratio of this novel material, its properties, the tunability of the pore size and the scalability

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of the surface porosification process offer a game changing perspective for a large number of applications.

Keywords: 3D epitaxial graphene, porous SiC, UHV growth, Raman spectroscopy, electron tomography, HAADF-STEM

1. Introduction

Graphene's outstanding properties make it an ideal material to realize high performance devices and sensors. Its huge potential has driven a scientific revolution, first using pristine graphene for optoelectronic applications [1], flexible electronics [2, 3], graphene-based sensors [3, 4, 5, 6], and biological applications [7]. Large research efforts have been made to tailor the electronic properties of graphene through defect engineering [8, 9, 10] and chemical functionalization, in order to increase the application fields and to improve the performance of devices. However, the 2D nature of graphene represents a limit for many applications: catalysis [11], photoassisted water splitting [12], gas detection [13] and storage [14, 15], drug delivery [16], high performance electrodes [17, 18], supercapacitors [19, 20], battery **cathodes** [21], water treatment and filtration [22, 23], all would strongly benefit from a high surface-to-volume ratio and a 3D structure.

The technological importance of porous carbon materials is underlined by a vast literature which starts even before the graphene discovery [24], in the form of carbon foams which represent an economical solution. Such carbon foams are prepared by different routes: the pyrolysis of carbon precursors, the assembly of exfoliated graphite or graphene oxide, always with chemical additives [25]. Porous carbon materials are extensively utilized for latent heat thermal storage [26], for adsorption of large molecules [27], as thermal conductive materials [28], for electrodes [29, 30] and for supercapacitors [25, 20]. However, the material quality is much lower than epitaxial graphene **and the supporting SiC scaffold of the presented material improves the structural stability**. Cleaner, high-performance materials are graphene foams grown by chemical route or chemical vapour deposition (CVD) [31, 32], and porous graphene networks [33] which represent a first step toward enhanced material quality, but are still far from the quality of epitaxial graphene. A three-dimensional arrangement of pillared graphene for hydrogen storage [34] and mass sensor applications [35] has been theoretically investigated but never realized. Therefore, the possibility to achieve a 3D graphene assembly without

losing its outstanding 2D properties is an ongoing research challenge.

Here we propose a game changing approach that allows to overcome these technological bottlenecks. It is based on the idea to grow an epitaxial graphene layer on a three-dimensional backbone constituted by nano-porous crystalline SiC [36]. The epitaxial graphene is grown by thermal decomposition of the SiC in an ultra-high vacuum (UHV) environment, resulting in a native 3D graphene material with a light, but overall robust structure. This is to the best of the authors' knowledge the first approach for the realization of 3D epitaxial graphene, a novel idea which enables a new and versatile material.

Porous SiC (pSiC) [37] has been initially developed for the fabrication of SiC MEMS devices based on a novel surface micromachining process [38] and successively utilized to fabricate optical components, such as rugate mirrors [39] fully integrated into a 4H-SiC substrate. The fabrication process has already been optimized and allows to control the local definition of the pores as well as the degree of porosity with depth [40]. Moreover, the porosification technique allows obtaining stacked layers of different porosity, increasing the versatility of this material [41]. Commercial wafers of 4H-SiC(0001) and (000 $\bar{1}$) have been porousified to produce samples of porous SiC in both Si-face and C-face orientation. This technique is scalable to large area samples [42]. Pores show an interconnected nature in Si-face samples, while they are usually in channel form in C-face samples. Therefore, samples could sustain a gas flow in the direction orthogonal to the surface, as far as pores dimension allows. Furthermore, porousified areas can be arranged in arrays, and it is possible to micromachine cavities in between porousified and dense 4H-SiC [38] or to detach the porous layer [41]. This versatility, and the coupling of a 3D graphene structure to a semiconductor backbone, opens up the possibility for a large number of applications in nano-electronics, sensors, and energy related fields.

2. Materials and Methods

The SiC wafers utilized in the porosification process, in both Si- and C-face samples, were doped with nitrogen and had a thickness of 350 μm . The Si-face samples had a resistivity of 0.106 Ohm-cm and the C-face samples had a resistivity of 0.02 Ohm-cm. In all cases the (0001) plane was exposed to etchant and wafers had a 4° off-axis orientation.

Porous SiC (Si-face) is obtained by utilizing a metal-assisted photo-chemical etching (MAPCE) followed by photo-electrochemical etching (PECE) of 4H-SiC [42], as shown schematically in Figure 1(a). The MAPCE step is introduced to produce a porous layer in the μm range [42] and provides the initialization sites for the PECE process. To perform MAPCE step, 300 nm thick Pt pads are deposited on the wafer, which act as local cathode and low-resistance charge transfer to the electrolyte. An etching solution containing $\text{Na}_2\text{S}_2\text{O}_8$ (0.15 mol/L) and HF (1.31 mol/L) is then used for MAPCE, performed in a standard electro-chemical cell (AMMT GmbH). A 250 W ES280LL mercury lamp at full spectrum was used as UV source in both MAPCE and PECE steps. After MAPCE, a PECE [42] step is performed. The porousification of C-face samples is obtained with a single PECE step. During PECE, a solution containing HF (5.52 mol/L) and ethanol (1.7 mol/L) is utilized, while UV exposure is running. **The porous layer thickness, measured with SEM randomly picking sites along the samples, was 21.1 μm and 18.9 μm for Si- and C-face samples, respectively.**

Porous SiC samples were analyzed via Scanning Electron Microscopy (SEM) (see Figure 1(b) and 1(e)), TEM and electron tomography (see Figure 1(d) and 1(g)), and Raman spectroscopy (see Figure S5). SEM images of the samples were obtained with a Zeiss Merlin SEM operated at 5 keV. For each sample, the surface morphology was investigated from plane-view SEM micrographs taken from at least ten different areas of the sample. SEM images of the porous 4H-SiC Si-face sample show a very rough surface with a "dendritic" structure (see Figure 1(b)), produced by the MAPCE step. The C-face sample shows a different morphology. The surface is almost flat, due to the absence of the MAPCE passage, with craters well visible in Figure 1(e). Raman spectroscopy and mapping was performed by using a Renishaw confocal microscope with a 100 \times objective (NA 0.85) equipped with a 532 nm laser as excitation source. The laser power is 1 mW on a spot size of 1 μm . The scan area is 20 \times 20 μm^2 , with 400 pixels, and a lateral pixel size of 1 μm . **The Raman analysis has been performed on the sample as it is, therefore the results reported in the following refer to the sample portion accessible to the Raman probe, namely about 700 nm sampling depth.**

The graphene growth was performed by thermal decomposition of the SiC, annealing the samples in an UHV environment with a base pressure of 5×10^{-11} mbar. It was performed by direct current heating with a Keithley 2260B power supply, while temperature was monitored with a J.S.C. TSS-F1-C3000 optical pyrometer. **Samples are mounted on two tantalum contacts**

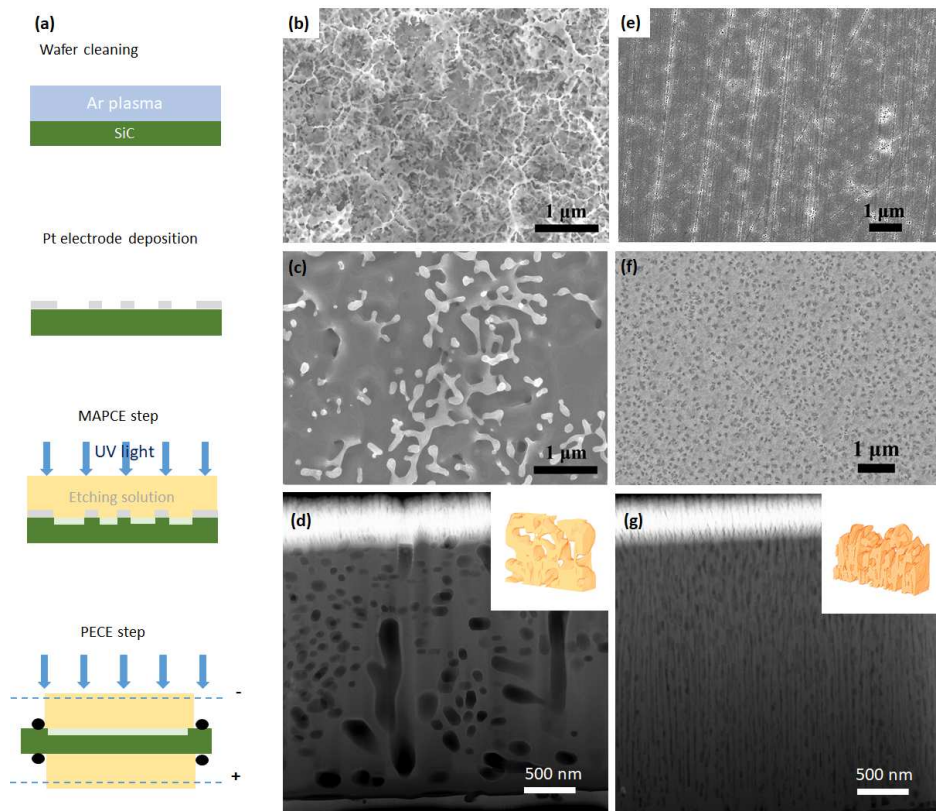


Figure 1: (a) Scheme of the MAPCE-PECE porosification process. (b) Plane-view SEM images of a Si-face sample from a 4H-SiC wafer after the porosification and (c) after the UHV annealing at 1370° C. (d) Cross-sectional TEM image of a Si-face sample, after the UHV annealing at 1370° C, prepared by FIB; the inset shows the electron tomography reconstruction of the pore structure. (e) Plane-view SEM images of a C-face sample from a 4H-SiC wafer after the porosification and (f) after the UHV annealing at 1260° C. (g) Cross-sectional TEM image of a C-face sample, after the UHV annealing at 1260° C, prepared by FIB; the inset shows the electron tomography reconstruction of the pores structure.

ensuring the current flow. Once the target temperature had been reached, it was kept constant for 2 minutes before cooling down to room temperature. The optimal growth temperature and annealing time depend on the use of the C- or Si-face of the wafer. For both the Si- and C-face, up to about 1140° C graphene does not grow, and Raman spectra are virtually identical to those of the starting pSiC sample. For Si-face samples, graphene starts to show a Raman signature after an annealing at temperatures higher than 1320° C and reaches its highest quality at 1370° C. At even higher temperatures the quality remains constant up to 1470° C, the highest explored temperature. As expected, the growth window for C-face samples is much narrower. Graphene starts to grow at 1220° C, reaches its best quality at 1260° C, but becomes graphitic already at 1300° C.

On the Si-face samples, graphene growth by thermal decomposition produces a surface reordering due to Si evaporation from the tiny branches of the dendritic structure, as clearly shown in Figure 1(c) and S1 (a,b). After the graphene growth on the C-face sample, the surface does not change much, and reordering is less evident, as shown in Figure 1(f) and S1 (c,d).

The pore structure in a sample section has been investigated by preparing proper lamellas by Focused Ion Beam (FIB) and analyzing them by electron tomography in High-Angle Annular Dark Field - Scanning Transmission Electron Microscopy (HAADF-STEM) mode for the 3-D characterization of the porous SiC. The analysis has been performed using a Thermo Fisher Scientific Titan microscope, equipped with an aberration corrector and a monochromator. The ability of the microscope to work at different voltages (80 kV to 300 kV) and the presence of a monochromator provides enough flexibility to control and minimize electron beam damage to the sample for the tilt series acquisition as well as for the visualization of the graphene layers at high resolution, discussed in the following. HAADF electron tomography tilt series were acquired by using a Fischione 2020 tomography holder between -75° and 75° with a step of 3° . The projection images were acquired at a magnification of 80 k \times and image resolution of 2048×2048 pixels. The obtained series were aligned using cross-correlation, and 3D reconstructions were obtained using the Expectation Maximization (EM) algorithm, as implemented in Astra Toolbox [43]. The reordering of the 3D pore structure is shown in Figures 1(d) and (g). Pore dimensions of the Si-face sample, obtained by electron tomography and shown in the inset to Figure 1(d), are peaked at around 80 nm in diameter, with an average of 180 nm (see Figure S2). The pores occupy about 33% of the sample volume. In the C-face

sample, pores are smaller, see Figure 1(g), and their diameter peaks between 30 and 70 nm, with an average of 85 nm. The pores occupy about 42% of the sample volume. The pore volume of both samples was obtained from tomography studies performed over a 100 ± 50 nm thick FIB sample of a representative area of $1 \times 1 \mu\text{m}$. Porosity usually changes with depth and etching parameters. At constant etching parameters the maximum variation in porosity with depth is less than 30% [41, 42]. Further details are given in the Supporting Information.

3. Results and discussion

Si-face samples. To demonstrate that graphene growth was successful in the 3D pores of the porous material, the FIB technique has been utilized to obtain lamellas from the Si-face sample. Observing the sample at 300 kV in TEM, the roundish pore morphology is evident in Figure 2(a) and (b). The FFT analysis of Figure 2(b) shows the (001) 4H-SiC array with points spaced by 1.01 nm, with two extra spots encircled in yellow (see inset on Figure 2(b)). These extra spots do not belong to the dominant crystal structure and correspond to an interplanar distance of 0.34 nm, consistent with the graphene inter-layer distance, which could indicate a stacking of the graphene sheets inside pores. To confirm this hypothesis, TEM analysis at lower electron energies (80 keV) has been performed, which avoids sample degradation due to the electron beam and allows to observe the graphene signature inside the pores. Figure 2(c) shows a higher resolution image of one of the pores where the SiC crystal structure can be seen at the outer part of the hole while the inner part appears to be composed of amorphous carbon, where it is possible to observe graphene layer stacking, especially in the higher magnification image on Figure 2(d) marked by yellow arrows, with an interspace distance of about 0.34 nm, in agreement with literature values for graphite.

Raman analysis shown in Figure 3(a) consistently confirms the successful synthesis of graphene on the porousified Si-face 4H-SiC substrate. The spectrum presents the three prominent Raman modes of graphene, the D peak (at 1359 cm^{-1}), the 2D peak (at 2716 cm^{-1}), and the G peak (at 1592 cm^{-1}) [44, 45]. We observed also the presence of a shoulder to the G peak due to the D' defect activated Raman mode [46]. The 2D peak has a single Lorentzian shape and a full width at half-maximum (FWHM) of $\approx 55 \text{ cm}^{-1}$, being the single Lorentzian shape a signature of single-layer graphene [44].

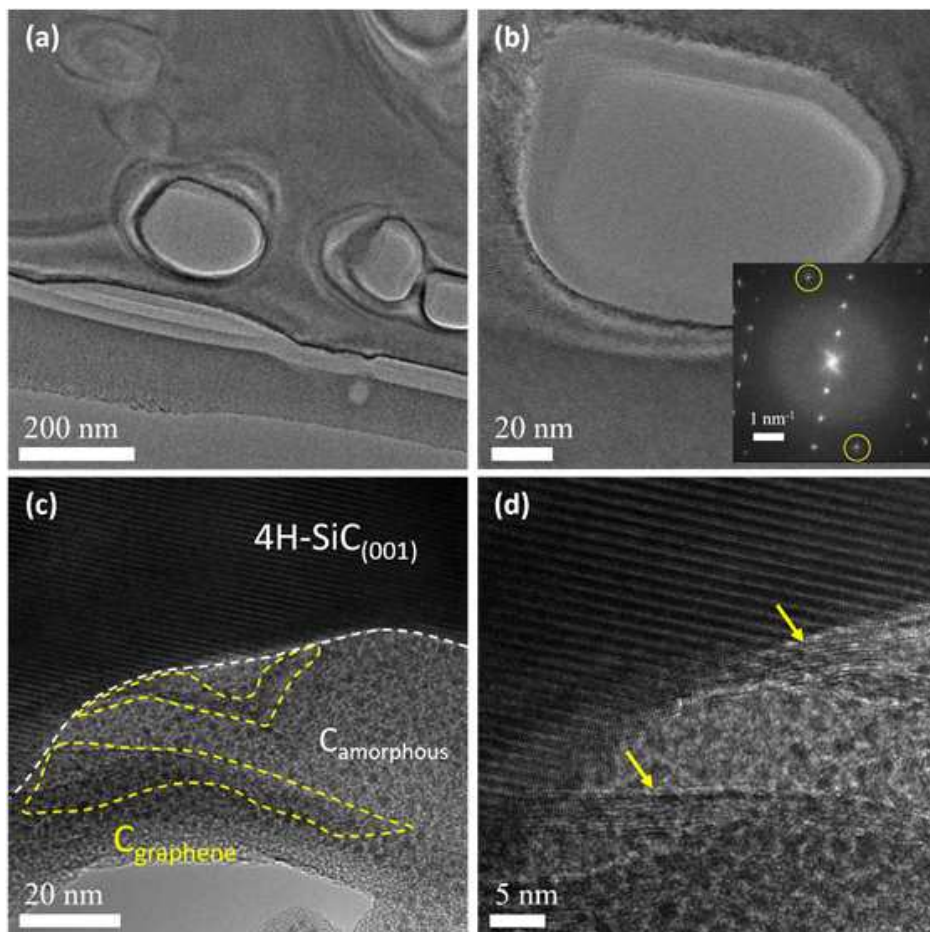


Figure 2: High resolution TEM images of a Si-face sample after graphene growth at 1370°C . (a) TEM image at 300 kV from an area $826\text{ nm} \times 826\text{ nm}$ showing multiple pores, (b) magnification to $152\text{ nm} \times 152\text{ nm}$ on one of the pores, inset FFT analysis with two extra spots of possible (002) graphene spacing encircled in yellow. (c) TEM image at 80 kV from an area of $81\text{ nm} \times 81\text{ nm}$ showing stacked graphene layers, indicated by the yellow ellipses, (d) $31\text{ nm} \times 31\text{ nm}$ on the left-central area in panel (c) where the stacked graphene layers are marked by yellow arrows.

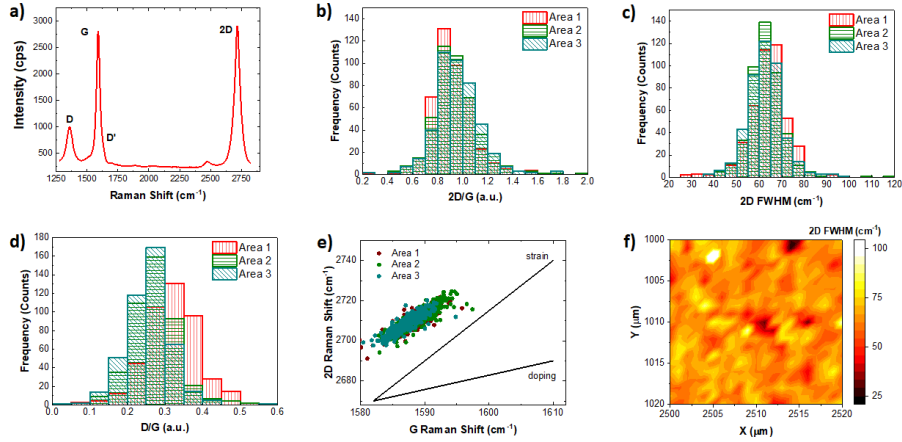


Figure 3: Raman analysis of a porous 4H-SiC Si-face sample after annealing at 1370° C. (a) Raman spectrum from 1250 to 2850 cm^{-1} (graphene peaks), (b) superposition of the 2D/G ratio histograms of three different areas on the sample, (c) analysis of the FWHM width of the 2D peak of the areas 1, 2 and 3 (superposition of the three histograms), (d) superposition of the D/G ratio histograms of the areas 1, 2 and 3, (e) Raman peak shift analysis, (f) map of the 2D peak width relative to area 1 as an example, full maps in SI.

In Figure 3(a), we underline the complete absence of the second order SiC-related Raman modes peaked at 1516 cm^{-1} and the two-phonon band at 1750 cm^{-1} [47]. In order to clarify the absence of these peaks, we analyze the range of Raman shifts related to the transverse optical (TO) phonons of 4H-SiC, discussed further on.

In order to evaluate the homogeneity of the graphene film, scanning Raman mapping has been carried out in 3 areas of the sample, with a size of $20 \mu\text{m} \times 20 \mu\text{m}$ for each area (see Figures S6 - S10). The statistical analysis of the standard benchmarks of graphene, such as the 2D/G intensity ratio, the 2D FWHM, and the D/G intensity ratio, obtained in the different areas, are presented in Figure 3(b), 3(c) and 3(d), respectively.

The histogram of the 2D/G intensity ratio, reported in Figure 3(b), shows a good homogeneity over the three different areas. In particular, the average values of the 2D/G intensity ratios are 0.90 ± 0.10 , 0.92 ± 0.10 , and 0.94 ± 0.10 for area 1, area 2 and area 3, respectively. The error associated to the average value is the FWHM of the Lorentzian curve, used to fit the histogram. The 2D/G ratio is normally employed for the evaluation of the

number of layers in graphene, but it is also strongly sensitive to the graphene doping. Therefore, considering the perfect Lorentzian fit of the 2D peak, indication of monolayer graphene, it is possible to deduce a high doping level of the graphene obtained on the porousified 4H-SiC substrate. Considering the 2D/G ratio around 1, we can hypothesize a doping concentration of $2 \times 10^{13} \text{ cm}^{-2}$ [48, 49]. A high electron doping of epitaxial graphene is well known and reported to be due to charge transfer from the substrate [50].

Another important benchmark for the quality of graphene is the width of the **single Lorentzian** 2D peak, used for the evaluation of the electron-electron scattering as well as the strain. This is important due to the complex morphology of the substrate on which the graphene is synthesized. The average FWHM of the 2D peak in the different areas is $64.0 \pm 5.0 \text{ cm}^{-1}$ (area 1) and $62.5 \pm 5.0 \text{ cm}^{-1}$ (areas 2 and 3). In Figure 3(f) the 2D FWHM of area 1 is reported as an example. ~~For epitaxial monolayer graphene obtained on flat 4H-SiC, the FWHM of the 2D peak is around 50 cm^{-1} [50].~~ **For epitaxial monolayer graphene obtained on flat SiC substrates, the FWHM of the 2D peak can vary between 30 cm^{-1} and 50 cm^{-1} [50, 51, 52].** The increase of the FWHM indicates a strain modification for the graphene obtained on the porousified Si-face 4H-SiC substrate [53].

The last standard benchmark for the graphene quality is the D/G intensity ratio. This parameter is important for both the defect nature [46] and their concentration [54] and the grain size in polycrystalline material [55]. The average value of the D/G intensity ratio is 0.32 ± 0.10 , 0.27 ± 0.10 , and 0.26 ± 0.10 for the different areas. These values correspond to an average grain size of graphene ranging from 70 nm to 100 nm [55].

In order to evaluate the strain-doping effects in the graphene film, Figure 3(e) presents the correlation plot of the Raman shift of the G peak with the Raman shift of the 2D mode. The dispersion of the different areas confirms a homogeneous n-type doping of the graphene, while there is an important spreading of the data along the strain direction. The slope of the linear fits for the different dispersions from areas 1 to 3 varies between 1.77 and 1.90. This is a clear indication for biaxial strain in the graphene layer, highly expected due to the complex nature of the substrate morphology. **The average Raman shift of the G (1587.2 cm^{-1}), compared to neutral and unstrained graphene (1581.5 cm^{-1}) [56] reveals a compressive strain of 0.25%, slightly higher than what previously demonstrated for epitaxial graphene on SiC substrates [57, 58, 59].**

In Figure 3(a), we report the absence of the second order SiC-related Ra-

man modes. In order to clarify the absence of these peaks, we analyze the range of Raman shifts related to the transverse optical (TO) phonons of 4H-SiC, reported in Figure S4. These peaks are well known to be related to the crystallinity of the SiC substrate. On the pristine Si-face 4H-SiC wafer, the Raman analysis shows prominent TO peaks and the presence of second order peaks, as reported in Figure S4. Figures S4 and S5 show that the porousification process and the high temperature process have a strong impact on the 4H-SiC substrate, as witnessed by a strong suppression of the intensity of the TO Raman modes at 775 cm^{-1} and 796 cm^{-1} , respectively. However, the peak widths demonstrate that the crystal quality of the porousified material is not deeply affected, and the peak widths and positions are in agreement with bulk crystals. The statistical analysis of the 4H-SiC TO peak is reported in Figure S7.

C-face samples. We have also investigated the graphene growth on the C-face of 4H-SiC. Several samples have been annealed at different temperatures, in order to find the best growth conditions. With respect to the Si-face, the C-face demonstrates a much more critical dependence on temperature, producing high quality graphene in a narrow range of about 40° C . The best graphene growth on the C-face samples has been achieved by annealing at 1260° C , which is lower than the optimal growth temperature on the Si-face.

As reported in Figure 1, the pore structure in C-face samples is quite different from that in Si-face samples. For the C-face, pores are distributed along "channels" starting from the surface toward the inner of the sample. Moreover, pores are rarely interconnected from channel to channel. At larger magnification, the channel structure is evident as shown in Figure 4(a). Figure 4(b), taken at a higher magnification, shows the elongated structure of a single pore. The FFT of this image does not show any extra spots (see inset on Figure 4(b)), in contrast to the Si-face sample. Moreover, pores are rarely found empty, but filled with amorphous carbon as shown in Figure 4(c). We attribute this fact to a plug effect. The pores are plugged by the fast Si evaporation rate, which results in a carbon accumulation in the pores, preventing the formation of graphene. Indeed, in C-face samples, no graphene sheets were observed inside the pores (see Figure 4(c)), while a graphene sheet stacking on the sample surface is observed, as shown in Figure 4(d), marked by yellow arrows.

Raman analysis of these graphene layers is reported in Figure 5. At a glance, it is evident that the graphene is more defective and less homogeneous than for the Si-face samples. Indeed, in a representative Raman spectrum

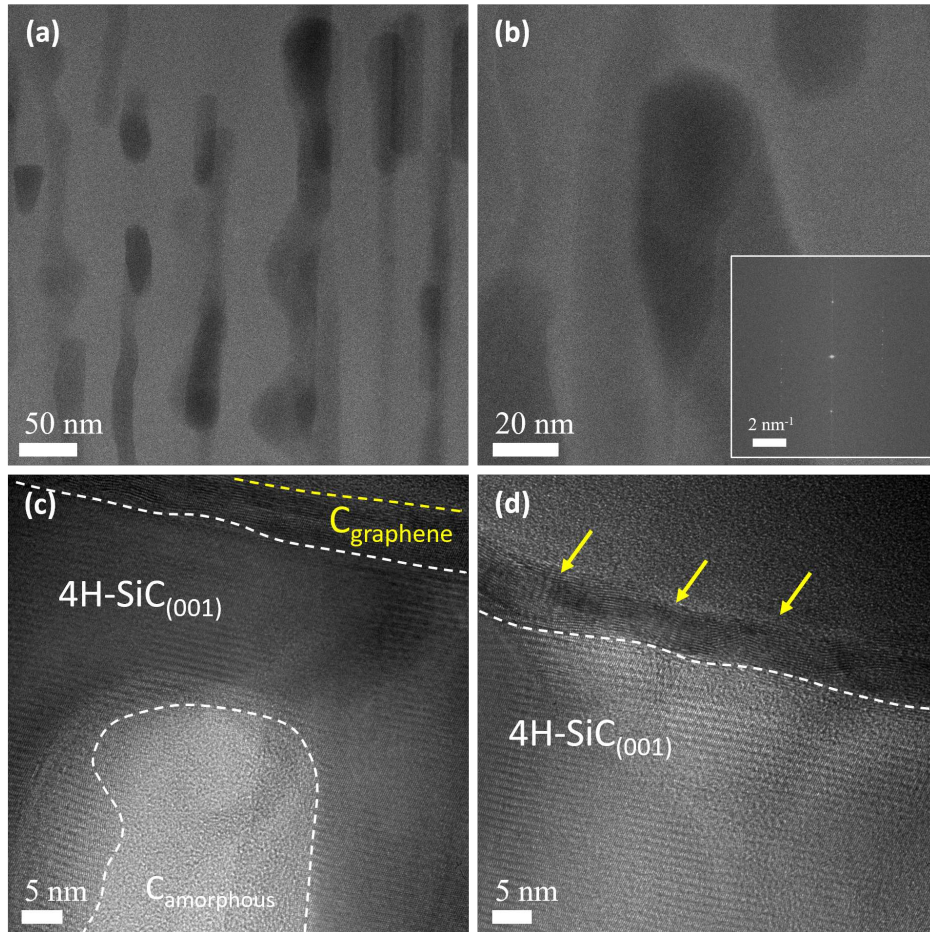


Figure 4: High resolution TEM images of a C-face sample after graphene growth at 1260° C. (a) TEM image at 80 kV on an area 395 nm × 395 nm showing multiple pores, (b) magnification at 140 nm × 140 nm on one of the pores, (c) magnification at 60 nm × 60 nm on a single pore, filled with amorphous carbon, (d) magnification at 60 nm × 60 nm. This last image shows the presence of graphene sheets on the sample surface, marked by yellow arrows.

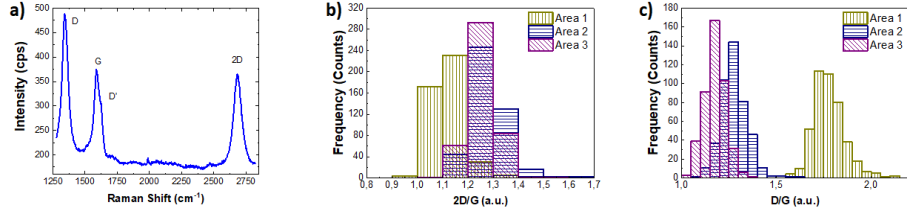


Figure 5: Raman analysis of a porous 4H-SiC C-face sample after annealing at 1260° C (a) Raman spectrum from 1250 to 2850 cm^{-1} (graphene peaks), (b) superposition of the 2D/G ratio histograms of the areas 1, 2 and 3, (c) superposition of the D/G ratio histograms of the areas 1, 2 and 3.

shown in Figure 5(a), the intensities of the D and D' Raman modes, peaked at 1341 cm^{-1} and 1615 cm^{-1} , are more intense, demonstrating a more defective graphene. The 2D mode is peaked at 2681 cm^{-1} with a FWHM of 75 cm^{-1} . The G peak appears at 1588 cm^{-1} . Raman mapping is carried out in three different areas, as indicated in Figures S12 - S17. The statistical analysis of the Raman maps, evaluating the different graphene benchmark parameters, is reported in Figures 5(b) and (c). In particular, Figure 5(b) presents histograms of the 2D/G intensity ratio, obtained in the different areas. The average values are 1.12 ± 0.1 , 1.27 ± 0.1 and 1.25 ± 0.1 for area 1, 2 and 3, respectively. These values suggest bilayer formation [50]. This claim is supported by the 2D FWHM analysis, reported in Figure S14. The D/G intensity ratio demonstrates a different grain size of graphene in area 1 with respect to areas 2 and 3. In fact, the average values of the D/G intensity ratio are 1.76 ± 0.1 (area 1), 1.27 ± 0.05 (area 2) and 1.18 ± 0.05 (area 3), yielding an average grain size of 2 nm in area 1, while in the other two areas the grain size is between 5 nm and 7 nm, about an order of magnitude smaller than for the Si-face. As will be discussed later, the observed inhomogeneities in these C-face samples are due to a temperature gradient in the sample that is large enough to produce an observable variation.

As reported for the Si-face, before the high temperature process, the Raman analysis of the porousified C-face 4H-SiC substrate shows prominent TO and LO peaks and the presence of second order peaks, as reported in Figure S11 of the Supporting Information. The Raman spectrum in the SiC range for the sample after graphene growth is reported in Figure S11(a). Even in this case, the 4H-SiC TO and LO modes are strongly suppressed

in intensity. The statistical analysis of the TO peak is reported in Figure S11(b). In addition, broad bands appear at about 300 cm^{-1} and 800 cm^{-1} (see S13(a)), indicating an amorphization of the SiC substrate [60].

The growth temperatures reported here are in agreement with those reported for 2D growth of graphene in 4H-SiC, between 1100 and 1450° C , with annealing times in the minutes range [61, 62, 63, 64]. In 2D samples it was found that the graphene quality increases if the Si surface is encapsulated, leaving a small calibrated leak which slows down the Si evaporation rate. This technique is referred to as confinement controlled sublimation (CCS)[65]. The porous SiC samples benefit from the CCS effect, since the pores confine the Si and act as leak regulators. Inside the pores, the graphene growth is also affected by the different crystal planes of the pore walls. Therefore, it not surprising that the graphene is stacked inside the pores, as observed in the HAADF-STEM images on Figure 2.

The growth of 2D graphene on C-face 4H-SiC is faster and less controllable than on the Si-face, and usually thick multilayers grow [66]. Moreover layers are made from rotationally disordered graphene sheets, producing a turbostratic graphene. Therefore, the 2D growth of a monolayer graphene from a C-face substrate is still a challenge. The lower growth temperature on the C-face with respect to the Si-face found in this work is in agreement with literature data [67].

On C-face 4H-SiC, the graphene growth process can still be optimized. Indeed, the graphene growth is less homogeneous at the surface, and most pores are filled with amorphous carbon. This is due to several factors. The pores of the C-face samples are smaller, and the growth window is narrower. We expect that a modified porosification protocol [68] for the C-face samples will result in larger pores, reducing the plugging effect. Moreover, an evaluation of the sample temperature along the length of the sample demonstrated a gradient of about 50° C (see supporting information for details). Such a gradient does not impact the growth in Si-face samples, which feature a larger growth window, but it strongly impacts the graphene growth on C-face samples, which have a narrower growth window. However, it will be straightforward to reduce the temperature gradient on the sample, which will result in a higher homogeneity of the graphene also on C-face samples.

4. Conclusion

We have demonstrated the growth of high quality epitaxial graphene on a nano-porous crystalline 4H-SiC(0001) for the first time. The porosification process and the graphene growth has been performed in both Si-face and C-face orientation. Porous samples were analyzed by SEM, TEM, electron tomography and Raman spectroscopy, in order to characterize the surface and pores structure and the graphene quality. The electron tomography allowed to measure an average pore diameter of 180 nm and 85 nm for Si-face and C-face samples respectively. The TEM analysis has demonstrated the graphene growth inside pores of Si-face samples and the Raman spectroscopy confirmed the high quality of graphene **in the sample portion accessible to the probe**, with a 2D/G ratio around 1 for both Si-face and C-face samples. The tunability of pore size and the scalability of the porosification process to large surface samples increase the flexibility of this material. The high surface-to-volume ratio and the native three-dimensional graphene on a semiconductor backbone represent a breakthrough for a huge number of applications in many fields as nano-electronic, sensors, energy materials and membrane technology.

CRedit authorship contribution statement

Stefano Veronesi: Conceptualization, Data Curation, Formal Analysis, Investigation, Supervision, Validation, Project administration and Writing - original draft. **Georg Pfusterschmied:** Data Curation, Formal Analysis and Investigation. **Filippo Fabbri:** Data Curation, Formal Analysis and Investigation. **Markus Leitgeb:** Data Curation, Formal Analysis and Investigation. **Omer Arif:** Data Curation, Formal Analysis and Investigation. **Daniel Arenas Esteban:** Data Curation, Formal Analysis and Investigation. **Sara Bals:** Project administration and Validation. **Ulrich Schmid:** Project administration and Validation. **Stefan Heun:** Project administration, Supervision and Validation. **All the authors:** Visualization and Writing - review and editing.

Declaration of competing interests

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

Acknowledgements

The authors want to acknowledge L. Covaci for helpful discussion. The authors acknowledge funding from the EU (ERC Consolidator Grant RE-ALNANO 815128 and Grant Agreement No. 731019 (EUSMI) under the Horizon 2020 Programme).

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